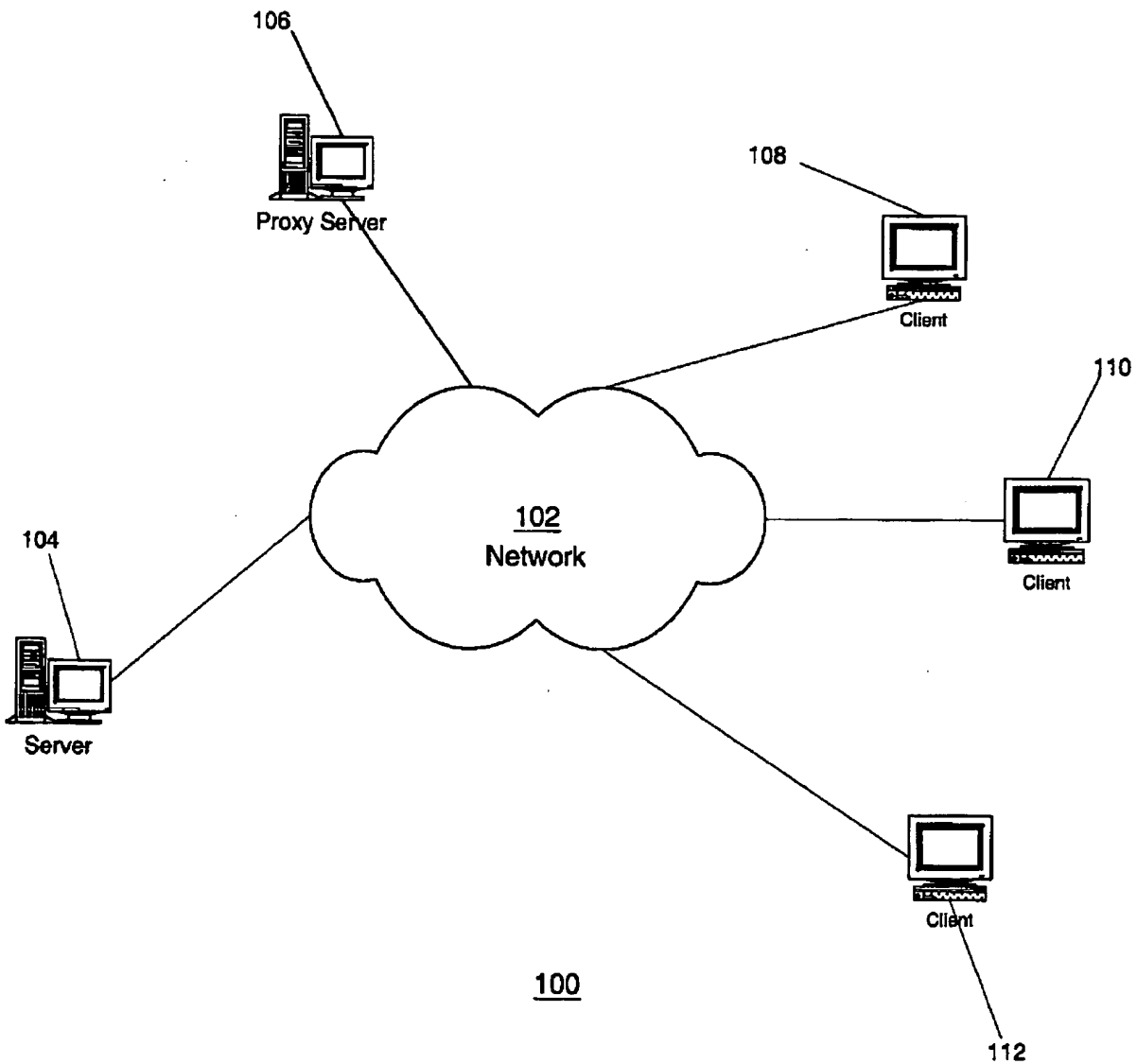


# Figure 1

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The diagram illustrates a computer system architecture. At the top, two processors, Processor 202 and Processor 204, are connected to a horizontal System Bus 206. Below the system bus is a block containing a Memory Controller/Cache 208 and an I/O Bridge 210. Local Memory 209 is connected to the memory controller/cache. A vertical I/O Bus 212 extends from the I/O bridge. Connected to the I/O bus are a Graphics Adapter 230 and a Hard Disk 232. On the right side, three PCI Bus Bridges (214, 222, and 224) are connected to the I/O bus. Each bridge is connected to a horizontal PCI Bus. Bridge 214 is connected to a Modem 218 and a Network Adapter 220. Bridge 222 and Bridge 224 are also connected to their respective PCI buses. The entire system is labeled 200.

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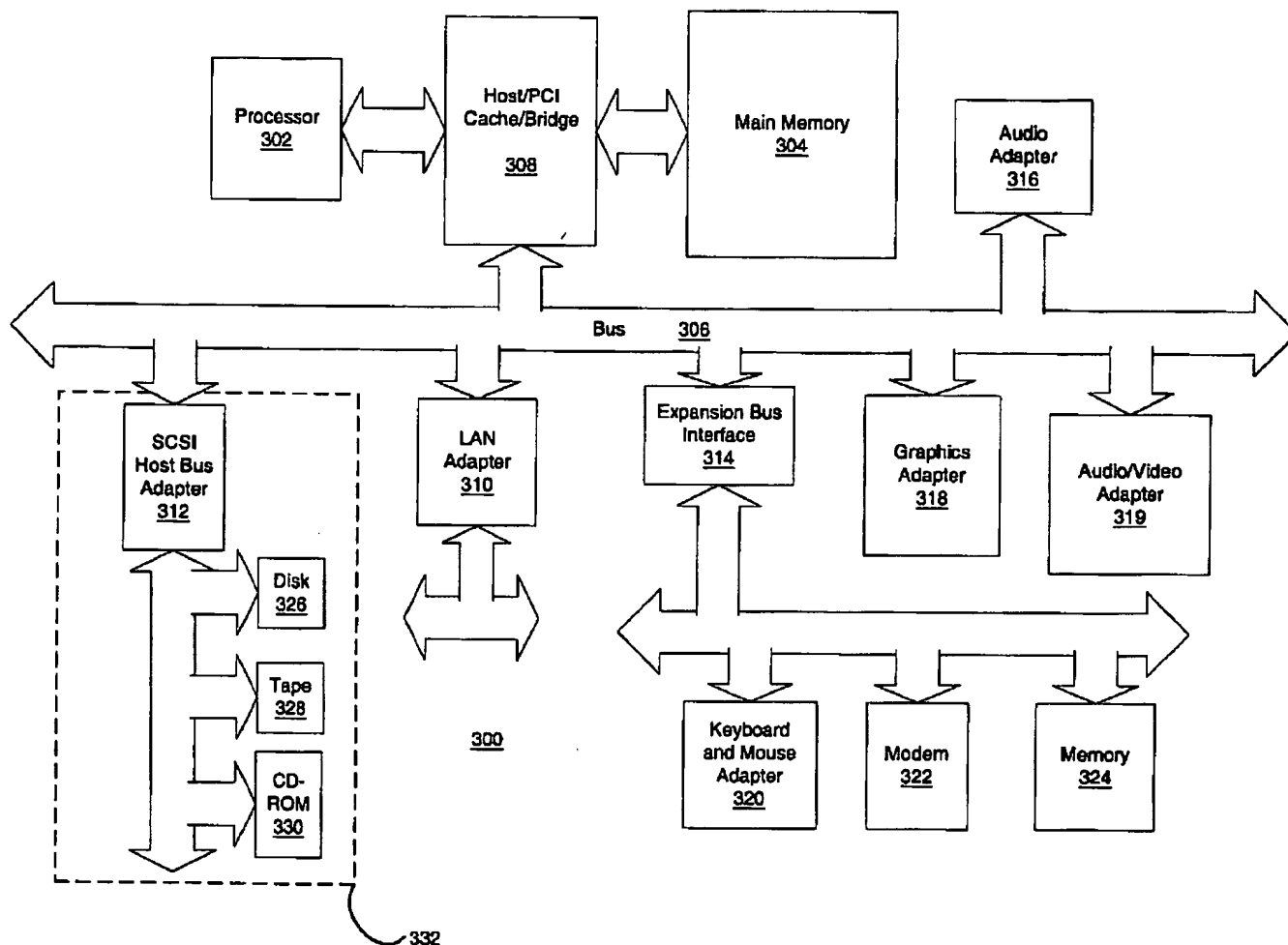


Figure 3

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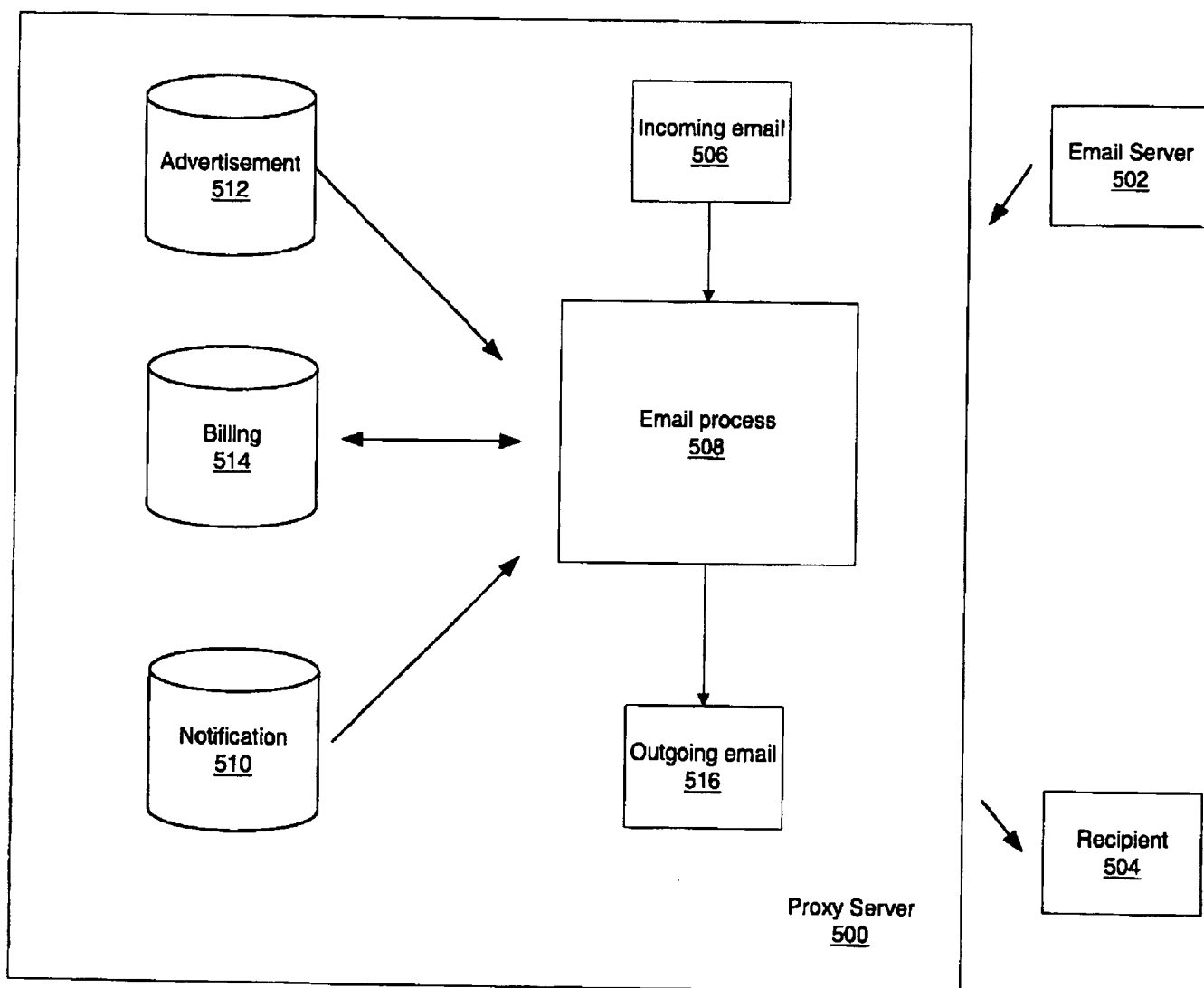
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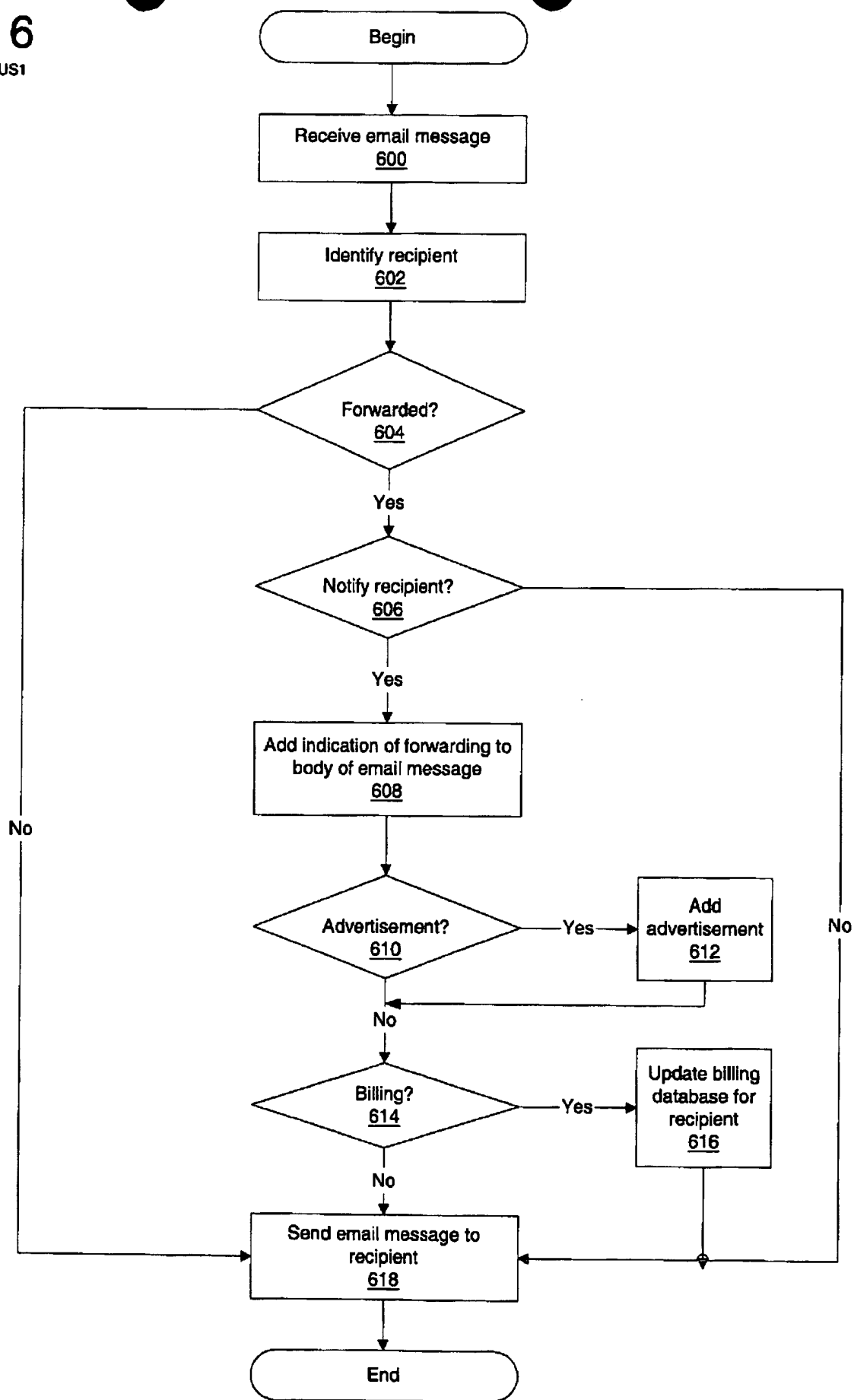
# Figure 5

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**Figure 6**  
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